



How to master Assembler Code? Work on it as easy as on COBOL

ITP Software
Systems Ltd.

Master your software with ITP-PANORAMA

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Introduction:

Many companies still have a significant part of their applications code in Assembler.

Assembler Programs are extreme problematic to maintain. (Never touch a running system)

Searches in high-level programming languages like Cobol or PL/I consume some 40% of the total time for a change in an application. This search time can be reduced by 90-95% using ITP-PANORAMA.

Searches in Assembler programs eat up nearly 80% of the money spent for any change.

Using ITP-PANORAMA the search time in Assembler programs can be reduce by 50%.

History:

Before 1980 Assembler was very common. Performance critical modules had to be coded in Assembler to make applications fast enough on the hardware of this time.

Other programming languages did not offer access to system functions.

Assembler did not have restrictions of higher level languages like dynamic linkage and the ability to modularize.

The Challenge:

The number of developers who are familiar with Assembler decreases rapidly. The new generation of developers had no opportunity to learn this language.

Even small changes can cost a lot of money which prevents changes from being made.

-Assembler programs are blocking considerations of migration. (new hardware, data base etc.)

-Various addressing modes (AMODE24, AMODE31 ...) cause dependencies on calls, linkage and so on.

ITP-PANORAMA Technology

Assembler programs can be scanned together with programs written in higher languages. This allows searches across the entire application system. The user interface shows Assembler very much like Cobol.

ITP-PANORAMA can handle Assembler specifics:

Assembler statements referring to a memory location are handled like statements in other languages together with the addressed symbol (location).

Symbol tables are built up the same way they are used by the Assembler compiler, with all attributes like start address, length and type.

The presentation, as far as possible, follows the rules of COBOL / PL/I record definitions with level number and re-definition information. This is based on a specific handling of DSECT's and ORG-statements.

Handling of Assembler Macros:

User macros are expanded so that statements and symbols that may contain generated statements and symbols are displayed. A reference is maintained to the original record in the macro. This can be tracked to understand which path in the macro produced the information.

Some system-macros are interpreted by the scanner (DCB, OPEN, GET, PUT, LOAD, ATTACH ...). They are presented like similar functions in high-level languages. File-access, calls between modules (often including a parameter list) are recognized.

The remaining macros are expanded. To achieve this the system-macro libraries (SYS1.MACLIB, IMSLIB etc) need to be loaded from the host.

Parameters passing thru registers often use constructs like:

```
LA    R1,Symbol
OPEN (R1)
```

and similar sequences. A small register memory helps to present the right symbol as a reference for the macro. This memory will be cleared at all levels.

Hints for Re-Implementation:

Studying the Assembler part of large application systems lead us to the following guidance:

1. Programs that can be re-written using higher level languages

Data manipulation programs that are today written in a high-level language would show information on the environment and also the associated records (Files, IMS segments, DB2 table accesses)



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Using ITP-PANORAMA the presentation is quite similar to high-level languages. Therefore it is possible to rewrite the code almost immediately. Even computation between fields can be followed up if the programmer systematically used symbols for addressing.

2. Programs that use system functions

In programs that are heavily using system macros, ITP-PANORAMA shows which macros are used together with the parameter preparation. In this case it could be feasible to rewrite such a program in C, where most of the system functions exist in the form of run time functions.

Migrating to another platform can often eliminate the need for such programs (the same functionality can be achieved in a different way).

3. Nearly un-understandable programs

In the case of very chaotic programs that are mainly working with registers, it may help to study the high-level programs calling them and trying to understand the functions from the usage.

Such programs can be used to implement string

manipulation functions missed in higher level languages. Understanding the functionality, they can be easily rewritten in C.

Advantages of using ITP-PANORAMA:

If programs fall under one of the above categories, it is possible to estimate the time that a reimplementation will take.

In many cases reimplementation can be done fairly mechanically by just following the displays of ITP-PANORAMA.

Even a JAVA developer can navigate thru an application written in Assembler because the user interface is presenting the code in an easy to understand fashion.

ITP-PANORAMA is an absolutely necessary tool if Assembler programs have to be examined and understood.

ITP-PANORAMA will take Assembler out of its isolation and let a developer see applications written in Assembler as part of the entire system.

The screenshot shows the ITP-PANORAMA interface with several panes:

- Program (1/4451):** A list of programs with 'BTXH401' selected.
- AccessType (1/):** A list of access types with 'FILE' selected.
- Statement (6/255):** A list of statements with 'OPEN ((R2), (INPUT))' and 'OPEN ((R2), (OUTPUT))' highlighted by a red box.
- Modifier:** A list of modifiers including DDNAME, DSORG, EODAD, MACRF, PARAM1, PARAM2, PARAM3, PARAM4, and SYNAD.
- Statements:** A table showing source lines and their corresponding assembly code. The highlighted lines are:

SourceLine	Statements
BTXVORL DCB DSORG=PS,DDNAME=BTXVORL,MACRF=(GM),EODAD	
BTXVORLN DCB DSORG=PS,DDNAME=BTXVORLN,MACRF=W,	
	SYNAD=ERROR1
	OPEN ((R2), (INPUT))
	OPEN ((R2), (OUTPUT))
	GET BTXVORL,VSATZ

Level	Symbol	Debitare	Percont	DioncedBy	Debito	Cffoct	length	ID
01	VFJ751	D3E3T			1	C	106	2675
02	VVFJ751	CL1D5	VFUC751		1	C	106	2675
02	FILLER	OR3	VVFUC751		1	C	106	2675
03	VFJMT	CL1	VVFUC751		1	C	1	2675
03	VKZ3Y7	CL1	VVFUC751		1	4	1	2675
03	VKZKMP	CL1	VVFUC751		1	5	1	2675
03	VK1911V*	11.54	VVFUC751		1	F	64	2675
03	FILLER	OR3	VKCKINF		1	6	64	2675
04	VDETYPE	CL3	VKCKINF		1	6	8	2675
04	VKDMI1	CL3	VKCKINF		1	14	8	2675
04	VKJMLJ2	CL3	VKCKINF		1	22	8	2675
04	VKDMI3	CL3	VKCKINF		1	30	8	2675
04	VKDMI34	CL3	VKCKINF		1	38	8	2675
04	VKDMI35	CL3	VKCKINF		1	46	0	2675
04	VKDMI35	CL1	VKCKINF		1	54	1	2675
04	VK1911V	11.1	VK1911V*		1	55	1	2675
04	VKDMI3	CL5	VKCKINF		1	56	6	2676
04	VKDMI39	CL3	VKCKINF		1	C2	0	2676
03	FILLER	OR3	VKCKINF		1	6	64	2676
04	VH1911V*	11.54	VK1911V*		1	F	64	2676

```

&A          MACRU
VFU0751
*****
&A.VFU0751  DS  CL106
          ORG  &A.VFU0751
          DS  CL14
          DC  111'11'
          DC  CL1'
          DS  CL64
          ORG  &A.KOMINF
          DC  &A.DETYPE
          DC  &A.KDMI01
          DC  &A.KDMI02
          DC  &A.KDMI03
          DC  &A.KDMI04
          DC  &A.KDMI05
          DC  &A.KDMI06
          DC  &A.KDMI07
*****

```

Symbol 118/1-F/4411

FILLER

VDETYPE

VFU0751

VFJMT

VH1911V*

VKDMI01

VKDMI02

VKDMI03

VKDMI04

VKDMI05

VKDMI06

Address : /1)

0

14

DIFFSEPT. (14)

0

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